Sheet 1 of 1

INFORMATION DISCLOSURE CITATION

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1275-49	10/070,802
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March 12, 2002	2637

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
EG		2 769 434 A1	4-1999	FR				X

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

EG		European Search Report mailed May 4, 2005 in corresponding EP application no. 00908047.4-1247-JP0001523
EG		Shung et al, "VLSI Architectures for Metric Normalization in the Viterbi Algorithm", International Conference on Communications including Supercomm Technical Sessions, Atland, April 15-19, 1990, New York, IEEE, US, vol. VOL 4, 15 April 1990, pp. 1723-1728

*Examiner [signature] Date Considered 5/8/06

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